

TIME TABLE

Semester: EVEN

Year: 2025-2026

Class: M. TECH-I (CS & VL)

Day	1	2	3	4		5	6	7	8
	8.30 – 9.20	9.30 – 10.20	10.30 – 11.20	11.30 – 12.20	12.20-2.00	2.00 – 2.50	3.00 – 3.50	4.00 – 4.50	5.00 – 5.50
Monday	A1 SPA	B2 WC RTS	C3 ON AVD			G5 IPCV	H6 DL	I7 FSPM	J8 VSD
Tuesday			D3 WC VSD	E4 AVD		H5 DL	I6 5G Tec.	J7 FSPM	K8 RTS
Wednesday	C1 WC	D2 IPCV	E3 ON	A4 5G Tec.				K7 FSPM	G8 AVD
Thursday			P6 (VSD)			P15 (RTS)			
Friday	E1 RTS	A2 VSD	B3 ON					G7 SPA SOC	H8 SPA SOC
	P9 (WC)					P17 (AVD)			
								H7 DL SOC	I8 IPCV
						P19 (IPCV)			

Sr. No.	Slot	Title	Faculty	Credit	Class Room
(1)	G5,J8	IMAGE PROCESSING AND COMPUTER VISION(ECCS102)	KPU,KPU,KPU	3-0-0	EC202N
(2)	D2				EC402N
(3)	C1,B2,D3	WIRELESS COMMUNICATION (ECCS104)	SG,SG,SG	3-0-0	EC402N
(4)	C3,E3,B3	OPTICAL NETWORKS (ECCS112)	ASM,ASM,ASM	3-0-0	EC402N
(5)	A3,A4	5G WIRELESS TECHNOLOGIES (ECCS128)	UDD,UDD,UDD	3-0-0	EC402N
(6)	I6				EC202N
(7)	A1,G7,H8	SPEECH PROCESSING AND APPLICATIONS (ECCS130)	SD,SD,SD	3-0-0	EC402N
(8)	H5,H6,H7	DEEP LEARNING, THEORY AND PRACTICE (ECCS172)	KC,KC,KC	3-0-0	EC202N
(9)	C3,G8,E4	ANALOG VLSI DESIGN (ECVL102)	AA,AA,AA	3-0-0	EC222N
(10)	B2,E1,K8	REAL-TIME SYSTEMS (ECVL104)	MCP,MCP,MCP	3-0-0	EC222N
(11)	J8,D3,A2	VLSI SYSTEM DESIGN (ECVL116)	NC,NC,NC	3-0-0	EC222N
(12)	G7,H8,H7	SOC DESIGN (ECVL118)	PJE,PJE,PJE	3-0-0	EC222N
(13)	I7,J7,K7	FUNDAMENTALS OF SEMICONDUCTOR PACKAGE MANUFACTURING AND TEST (ECVL178)	VG,L1,L1	3-0-0	EC106N
(14)	P19	IMAGE PROCESSING AND COMPUTER VISION(ECCS102)	KPU,P20	0-0-2	EC307N
(15)	P3,P9	WIRELESS COMMUNICATION (ECCS104)	SG,P10	0-0-4	EC307N
(16)	P17	ANALOG VLSI DESIGN (ECVL102)	AA,P13	0-0-2	EC306N
(17)	P15	REAL-TIME SYSTEMS (ECVL104)	MCP,P18	0-0-2	EC307N
(18)	P6	VLSI SYSTEM DESIGN (ECVL116)	NC,P18	0-0-2	EC306N

